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Term	Documents
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<u>L28</u>	L23 and l9	166	<u>L28</u>
<u>L27</u>	L23 and l8	102	<u>L27</u>
<u>L26</u>	L23 and l7	602	<u>L26</u>
<u>L25</u>	L23 and l6	872	<u>L25</u>
<u>L24</u>	L23 and l5	872	<u>L24</u>
<u>L23</u>	l12 and (maxium or max or longest)	1510	<u>L23</u>
<u>L22</u>	l12 and l10	24	<u>L22</u>
<u>L21</u>	l12 and l9	212	<u>L21</u>
<u>L20</u>	l12 and l8	150	<u>L20</u>
<u>L19</u>	l12 and l7	863	<u>L19</u>
<u>L18</u>	l12 and l5	1247	<u>L18</u>
<u>L17</u>	l11 and l10	94	<u>L17</u>

<u>L16</u>	l11 and l9	262	<u>L16</u>
<u>L15</u>	l11 and l8	319	<u>L15</u>
<u>L14</u>	l11 and l7	1151	<u>L14</u>
<u>L13</u>	l11 and l5	1826	<u>L13</u>
<u>L12</u>	L11 and l4	2125	<u>L12</u>
<u>L11</u>	l2 and l3	10568	<u>L11</u>
<i>DB=PGPB,USPT; PLUR=YES; OP=OR</i>			
<u>L10</u>	(717/140-145,159)[CCLS]	1798	<u>L10</u>
<u>L9</u>	(712/210-213)[CCLS]	910	<u>L9</u>
<u>L8</u>	(711/202-221)[CCLS]	6301	<u>L8</u>
<u>L7</u>	(712/205-230,300)[CCLS]	7168	<u>L7</u>
<u>L6</u>	(712/2-300)![CCLS]	12964	<u>L6</u>
<u>L5</u>	(712/2-300)[CCLS]	12964	<u>L5</u>
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<u>L4</u>	(fetch\$5 or prefetch\$5 or issu\$4 or dispatch\$5) near25 decod\$3	20481	<u>L4</u>
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<u>L2</u>	(variable or different or adjust\$6) near6 (length or width or size)	1106142	<u>L2</u>
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IEEE CNF	IEEE Conference Proceeding
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 Chi Wu; Kun-Yuan Hsieh; Yung-Chia Lin; Chung-Ju Wu; Wen-Li Shih; S. C. Chen; Chung-Kai Chen; Yi-Ping You; Jeng Kuen Lee;  
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 Shimizu, N.; Naito, M.;  
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**6. Real-time MPEG-2 software decoding with a dual-issue RISC processor**

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